

Registration (Talya Hotel, Lobby) 08:30 - 17:30 from August 23rd to August 25th					
Hour					
Sunday August 23rd					
09:00 - 12:00	Hall Perge Sensor Electronic Interfaces (Part I) Lecturer: Franco Maloberti	Hall Aspendos Low-Power Design of CMOS Digital Integrated Circuits and Systems (Part I) Lecturer: Yusuf Leblebici	Hall Side Digital representation of audio, video and related data (Part I) Lecturer: Leonardo Chiariglione	Hall Attaleia CMOS-Circuits/Samples Interface: Improvements by Nanotechnology in Label-Free Diagnostics Lecturer: Sandro Carrara	Hall Hadrian
12:00 - 13:30	Lunch				
13:30 - 16:30	Hall Perge Sensor Electronic Interfaces (Part II) Lecturer: Franco Maloberti	Hall Aspendos Low-Power Design of CMOS Digital Integrated Circuits and Systems (Part II) Lecturer: Yusuf Leblebici	Hall Side Digital representation of audio, video and related data (Part II) Lecturer: Leonardo Chiariglione	Hall Attaleia Capacitive Sensing Technique Dedicated for Biodiagnostic Microsystems Lecturer: Mohamad Sawan	Hall Hadrian
Hour					
Monday August 24th					
09:00 - 10:40	Opening Lecture Leon O. Chua Memristor: 38 Years Later Chair: Cem Göknaar HALL Perge				
10:40 - 11:10	Coffee Break				
11:10 - 12:10	Hall Perge Franco Maloberti Ultra Low-Power Data Converters: Challenges and Design Solutions		Hall Aspendos Yusuf Leblebici Computing With Leakage Currents: Subthreshold Source-Coupled Logic Circuits		
12:30 - 13:30	Lunch				
13:30 - 15:10	Hall Perge Analog Filters I	Hall Aspendos Mixed Signal Circuit and Systems I	Hall Side Digital VLSI Circuit Design	Hall Attaleia CMOS Sensory Systems	Hall Hadrian Cellular Nonlinear Networks: Theory, Applications and Implementations
15:10 - 15:40	Coffee Break				
15:40 - 17:40	Hall Perge Analog Filters II	Hall Aspendos Non Linear Systems II	Hall Side VLSI Design for Digital Signal	Hall Attaleia New Trends in A/D Converters Design and Testing I	Hall Hadrian Focal-Plane Sensor-Processor Chip Implementation
18:00 - 20:00	Welcome Cocktail				
Hour					
Tuesday August 25th					
09:00 - 10:00	Plenary Lecture Georges Gielen Innovations in mixed-signal circuit design for advanced CMOS technologies HALL Perge				
10:00 - 10:30	Coffee Break				
10:30 - 12:10	Hall Perge Analog Integrated Circuits I	Hall Aspendos Wireless RFICs	Hall Side Circuit Theory	Hall Attaleia Nonlinear System Modelling	Hall Hadrian Neural Networks: Modeling & Design
12:10 - 13:30	Lunch				
13:30 - 15:30	Hall Perge Analog Integrated Circuits II	Hall Aspendos RF Circuits & Models	Hall Side Side Channel & Fault Attacks, Countermeasures	Hall Attaleia Oscillators & PLLs	Hall Hadrian Biomedical Circuits & Systems
15:30 - 16:00	Coffee Break				
16:00 - 17:40	Hall Perge Analog Integrated Circuits III	Hall Aspendos Mixed Signal Circuit & Systems II	Hall Side Computational Methods	Hall Attaleia	Hall Hadrian
Hour					
Wednesday August 26th					
09:00 - 10:00	Plenary Lecture Inci Delemen A Guided tour in History Perge, Side, and Other Ancient Cities in Pamphylia HALL Perge				
10:00 - 17:00	Excursion to the Past				
20:00 - 23:00	Gala Dinner				
Hour					
Thursday August 27th					
09:00 - 10:00	Plenary Lecture Manfred Glesner Organic Electronic Circuit Design HALL Perge				
10:00 - 10:30	Coffee Break				
10:30 - 12:10	Hall Perge Oscillators & Filters	Hall Aspendos Computational Tools	Hall Side Mixed Signal Circuit and Systems III	Hall Attaleia Nonlinear Systems I	Hall Hadrian Fault Diagnosis
12:10 - 13:30	Lunch				
13:30 - 15:30	Hall Perge Analog Filters & Amplifiers	Hall Aspendos CAD for IC	Hall Side Communication Circuits	Hall Attaleia Nonlinear Circuits	Hall Hadrian New Trends in A/D Converters Design & Testing II
15:30 - 16:00	Coffee Break				
16:00 - 17:40	Hall Perge	Hall Aspendos VLSI Modeling & Design	Hall Side Audio & Image Processing	Hall Attaleia Advances in AMS/RF Design Methods	Hall Hadrian Emulated Digital CNN Based Computing on Different Array Processors